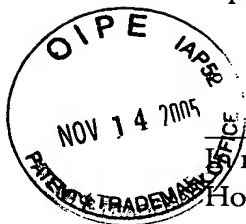


IFW

Application No.: 10/611,892

Docket No.: M4065.0646/P646



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Re Patent Application of:
Howard E. Rhodes

Application No.: 10/611,892

Confirmation No.: 3670

Filed: July 3, 2003

Art Unit: 2815

For: OPTIMIZED TRANSISTOR FOR IMAGER
DEVICE

Examiner: M. C. Landau

SUBMISSION OF FORMAL DRAWINGS

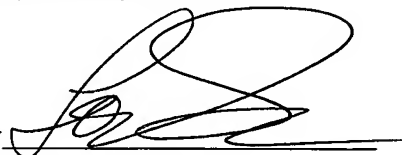
MS PGPUB Drawings
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Submitted herewith is one set (21 sheets, 23 figures) of formal drawings for filing in the above-identified patent application. Kindly substitute the enclosed drawings for the drawings submitted with the originally filed application.

Dated: November 14, 2005

Respectfully submitted,

By 

Thomas J. D'Amico

Registration No.: 28,371

Ryan H. Flax

Registration No.: 48,141

DICKSTEIN SHAPIRO MORIN &
OSHINSKY LLP

2101 L Street NW

Washington, DC 20037-1526

(202) 785-9700

Attorney for Applicant